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**INFORMATION DISCLOSURE  
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Sheet 1 of 1

**Complete if Known**

Application Number	10/577,284
Filing Date	May 7, 2007
First Named Inventor	Karsten STREHL
Art Unit	2123
Examiner Name	Benyam SOLOMON
Attorney Docket Number	10191/4565

**U.S. PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Document Number Number - Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

**FOREIGN PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		JP 2000020291	January 21, 2000			*
		JP 2001147830	May 29, 2001			*
		JP 10326203	December 8, 1998			*
		JP 2001051871	February 23, 2001			*
		JP 5035534	February 12, 1993			*

\*English language abstract provided herewith.

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		"Ascet-s Ascet-SD White Paper" 'Online September 2002, Seiten 1-29, XP002286572, Y-Schrift;	
		Stewart D B et al: "Dynamically reconfigurable embedded software - does it make sense?" Engineering of Complex Computer Systems, 1996, Proceedings, Second IEEE International Conference on Montreal, Quebec, Canada, 21-25 10.1996, Seiten 217-220, XP010201560, Y-Schrift;	
		Petters S et al: "The REAR framework for emulation and analysis of embedded hard real-time systems" Rapid System Prototyping, 1999, IEEE International Workshop o Clearwater, FL, USA, 16-18 06.1999, Seiten 100-107, XP010343112, A-Schrift;	
		Friedrich L F et al: "A survey of configurable , component-based operating systems for embedded applications" IEEE Micro, IEEE INC. New York, US, Vol. 21, No. 3, May 2001, Sieten 54-68, XP002963814, A-Schrift;	
		"Implementation and Evaluation of the Shaman Distributed Simulator for Shared Memory Multiprocessor" Joint Symposium on Parallel Processing JSPP 2002, May 2002.	

Examiner  
Signature

/Eunhee Kim/

Date  
Considered

03/10/2010

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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